## **SIEMENS**

## **Data sheet**

6ES7216-2AD23-0XB0

 $^{***}$  Spare part  $^{***}$  SIMATIC S7-200, CPU 226 Compact unit, DC power supply 24 DI DC/16 DO DC, 16/24 KB progr./10 KB data, 2 PPI/user-programmable interface



Supply voltage	
Rated value (DC)	
• 24 V DC	Yes
Load voltage L+	
<ul> <li>Rated value (DC)</li> </ul>	24 V
<ul> <li>permissible range, lower limit (DC)</li> </ul>	20.4 V
<ul> <li>permissible range, upper limit (DC)</li> </ul>	28.8 V
Input current	
Inrush current, max.	10 A; at 28.8 V
from supply voltage L+, max.	1 050 mA; 150 mA to 1 050 mA output current for expansion modules (5 V DC) 1 000 mA
Encoder supply	
24 V encoder supply	
• 24 V	Yes; permissible range: 15.4 to 28.8 V
Short-circuit protection	Yes; electronic at 400 mA
<ul> <li>Output current, max.</li> </ul>	400 mA
Power loss	
Power loss, typ.	11 W
Memory	
Number of memory modules (optional)	1; pluggable memory module, content identical with integral EEPROM; can additionally store recipes, data logs and other files
Work memory	
<ul><li>integrated (for program)</li></ul>	24 kbyte; 16 KB with active run-time edit
integrated (for data)	10 kbyte
Backup	
<ul><li>present</li></ul>	Yes; Program: Entire program maintenance-free on integral EEPROM, programmable via CPU; data: Entire DB 1 loaded from PG/PC maintenance-free on integral EEPROM, current values of DB 1 in RAM, retentive memory bits, timers, counters, etc. maintenance-free via high-performance capacitor; optional battery for long-term buffering
Battery	
Backup battery	
Backup time, max.	100 h; (min. 70 h at 40 °C); 200 days (typ.) with optional battery module
CPU processing times	
for bit operations, max.	0.22 μs
Counters, timers and their retentivity	
S7 counter	
Number	256
Retentivity	
— adjustable	Yes; via high-performance capacitor or battery
Counting range	
— lower limit	0

— upper limit	32 767
S7 times	
Number	256
Retentivity	
— adjustable	Yes; via high-performance capacitor or battery
Time range	
— lower limit	1 ms
— upper limit	54 min; 4 timers: 1 ms to 30 s; 16 timers: 10 ms to 5 min; 236 timers: 100 ms to
	54 min
Data areas and their retentivity	
Flag	00 h. t.
Size, max.      Determinists available.	32 byte
Retentivity available     A of which retentive with bettery.	Yes; M 0.0 to M 31.7
of which retentive with battery      of which retentive without bettery	0 to 255, via high-performance capacitor or battery, adjustable
of which retentive without battery  Hardware configuration	0 to 112 in EEPROM, adjustable
	7; Only expansion modules of the S7-22x series can be used. Due to the
Number of expansion units, max.	limited output current, the use of expansion modules may be limited.
connectable programming devices/PCs	SIMATIC PG/PC, standard PC
Expansion modules	
<ul> <li>Analog inputs/outputs, max.</li> </ul>	35; max. 28 inputs and 7 outputs (EM) or max. 0 inputs and 14 outputs (EM)
<ul> <li>Digital inputs/outputs, max.</li> </ul>	148; max. 128 inputs and 120 outputs (CPU+EM)
<ul> <li>AS-Interface inputs/outputs, max.</li> </ul>	62; AS-Interface A/B slaves (CP 243-2)
Digital inputs	
Number of digital inputs	24
Source/sink input	Yes; optionally, per group
Input voltage	
<ul> <li>Rated value (DC)</li> </ul>	24 V
• for signal "0"	0 to 5 V
● for signal "1"	min. 15 V
Input current	
● for signal "1", typ.	2.5 mA
Input delay (for rated value of input voltage)	
for standard inputs	
— parameterizable	Yes; all
— at "0" to "1", min.	0.2 ms
— at "0" to "1", max.	12.8 ms
for interrupt inputs	
— parameterizable	Yes; I 0.0 to I 0.3
for technological functions	
— parameterizable	Yes; (E 0.0 to E 1.5) 30 kHz
Cable length	F00 01 1 11 1 F00 11 1
• shielded, max.	500 m; Standard input: 500 m, high-speed counters: 50 m
• unshielded, max.	300 m; not for high-speed signals
Digital outputs	40.7
Number of digital outputs	16; Transistor
Short-circuit protection	No; to be provided externally
Limitation of inductive shutdown voltage to	1 W
Switching capacity of the outputs	0.75 A
with resistive load, max.      on lamp load, max.	0.75 A 5 W
on lamp load, max.  Output voltage	J VV
• for signal "1", min.	20 V DC
Output current	20, 20
• for signal "1" rated value	750 mA
for signal "0" residual current, max.	10 µA
Output delay with resistive load	
• "0" to "1", max.	15 μs; of the standard outputs, max. (Q 0.2 to Q 1.1) 2 μs; of the pulse outputs,
•	max. (Q 0.0 to Q 0.1) 2 µs
● "1" to "0", max.	130 $\mu$ s; of the standard outputs, max. (Q 0.2 to Q 1.1) 10 $\mu$ s; of the pulse
Develled and taking of the	outputs, max. (Q 0.0 to Q 0.1) 10 μs
Parallel switching of two outputs	

• for uprating	Yes
Switching frequency	165
of the pulse outputs, with resistive load, max.	20 kHz; Q0.0 to Q0.1
Total current of the outputs (per group)	20 (112, 00.0 to 00.1
all mounting positions	
— up to 40 °C, max.	6 A
horizontal installation	
— up to 55 °C, max.	6 A
Relay outputs	
Number of relay outputs	0
Cable length	
shielded, max.	500 m
• unshielded, max.	150 m
Analog inputs	100 111
	2: Analog potentiameter: recolution 9 hit
Number of analog potentiometers  Encoder	2; Analog potentiometer; resolution 8 bit
Connectable encoders	V
2-wire sensor      normically guidescent current (2 wire concer), may	Yes
— permissible quiescent current (2-wire sensor), max.	1 mA
1. Interface	late and a DO 405 interfere
Interface type	Integrated RS 485 interface
Protocols	V 4 MPL 4 7 4 4 9 PL
• MPI	Yes; As MPI slave for data exchange with MPI masters (S7-300/S7-400 CPUs, OPs, TDs, Push Button Panels); S7-200-internal CPU/CPU communication is possible in the MPI network with restrictions; transmission rates: 19.2/187.5 kbit/s
• PPI	Yes; with PPI protocol for program functions, HMI functions (TD 200, OP), S7-200-internal CPU/CPU communication; transmission rates 9.6/19.2/187.5 kbit/s
• serial data exchange	Yes; As freely programmable interface with interrupt facility for serial data exchange with third-party devices with ASCII protocol transfer rates: 1.2 / 2.4 / 4.8 / 9.6 / 19.2 / 38.4 / 57.6 / 115.2 kbps; the PC/PPI cable can also be used as RS 232/RS 485 converter
MPI	
Transmission rate, min.	19.2 kbit/s
Transmission rate, max.	187.5 kbit/s
2. Interface	
Interface type	Integrated RS 485 interface
Protocols	
• MPI	Yes; As MPI slave for data exchange with MPI masters (S7-300/S7-400 CPUs, OPs, TDs, Push Button Panels); S7-200-internal CPU/CPU communication is possible in the MPI network with restrictions; transmission rates: 19.2/187.5 kbit/s
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Integrated Functions	
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Number of alarm inputs	4; 4 rising edges and/or 4 falling edges
Number of alarm inputs  Number of pulse outputs	2; High-speed outputs, 20 kHz, with interrupt option; pulse-width and frequency modulation option
Number of alarm inputs  Number of pulse outputs  Limit frequency (pulse)	2; High-speed outputs, 20 kHz, with interrupt option; pulse-width and frequency
Number of alarm inputs  Number of pulse outputs  Limit frequency (pulse)  Potential separation	2; High-speed outputs, 20 kHz, with interrupt option; pulse-width and frequency modulation option
Number of alarm inputs  Number of pulse outputs  Limit frequency (pulse)	2; High-speed outputs, 20 kHz, with interrupt option; pulse-width and frequency modulation option
Number of alarm inputs  Number of pulse outputs  Limit frequency (pulse)  Potential separation	2; High-speed outputs, 20 kHz, with interrupt option; pulse-width and frequency modulation option 20 kHz  Yes
Number of alarm inputs Number of pulse outputs  Limit frequency (pulse)  Potential separation  Potential separation digital inputs	2; High-speed outputs, 20 kHz, with interrupt option; pulse-width and frequency modulation option 20 kHz
Number of alarm inputs  Number of pulse outputs  Limit frequency (pulse)  Potential separation  Potential separation digital inputs  • between the channels	2; High-speed outputs, 20 kHz, with interrupt option; pulse-width and frequency modulation option 20 kHz  Yes
Number of alarm inputs  Number of pulse outputs  Limit frequency (pulse)  Potential separation  Potential separation digital inputs  • between the channels  • between the channels, in groups of	2; High-speed outputs, 20 kHz, with interrupt option; pulse-width and frequency modulation option 20 kHz  Yes
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Ambient conditions	
Ambient temperature during operation	
<ul> <li>horizontal installation, min.</li> </ul>	0 °C
<ul> <li>horizontal installation, max.</li> </ul>	55 °C
<ul> <li>vertical installation, min.</li> </ul>	0 °C
vertical installation, max.	45 °C
Air pressure acc. to IEC 60068-2-13	
<ul> <li>permissible range, lower limit</li> </ul>	860 hPa
permissible range, upper limit	1 080 hPa
Relative humidity	
Operation, min.	5 %
<ul> <li>Operation, max.</li> </ul>	95 %; RH class 2 in accordance with IEC 1131-2
configuration / header	
configuration / programming / header	
Command set	Bit logic instructions, compare instructions, timer instructions, counter instructions, clock instructions, transmissions instructions, table instructions, logic instructions, shift and rotate instructions, conversion instructions, program control instructions, interrupt and communications instructions, logic stack instructions, integer maths, floating-point math instructions, numerical functions
<ul> <li>Program processing</li> </ul>	free cycle (OB 1), interrupt-controller, time-controlled (1 to 255 ms)
<ul> <li>Program organization</li> </ul>	1 OB, 1 DB, 1 SDB subroutines with/without parameter transfer
<ul> <li>Number of subroutines, max.</li> </ul>	64
Programming language	
— LAD	Yes
— FBD	Yes
— STL	Yes
Know-how protection	
<ul> <li>User program protection/password protection</li> </ul>	Yes; 3-stage password protection
connection method	
Plug-in I/O terminals	Yes
Dimensions	
Width	196 mm
Height	80 mm
Depth	62 mm
Weights	
Weight, approx.	550 g

IP20

last modified:

IP degree of protection

5/22/2024